



CFB600 SERIES

600 TO 700 WATTS 2:1 INPUT DC-DC CONVERTERS



FEATURES

- * 600 - 700W Isolated Output
- * Efficiency to 92%
- * Fixed Switching Frequency
- * Input under-voltage Protection
- * Over Temperature Protection
- * Over Voltage/Current Protection
- * Remote ON/OFF
- * Industry Full-Brick Package
- * Fully Isolated 1500VDC
- * UL60950-1 Approval



MODEL NUMBER	INPUT VOLTAGE	OUTPUT VOLTAGE	OUTPUT CURRENT		INPUT CURRENT		% EFF.	Capacitor Load max.
			MIN.	MAX.	NO LOAD	FULL LOAD		
CFB600-24S12	18-36VDC	12VDC	0mA	50 A	150mA	28.09A	88	10000µF ⁽²⁾
CFB600-24S24	18-36VDC	24VDC	0mA	25A	150mA	27.78A	90	5000µF ⁽²⁾
CFB600-24S28	18-36VDC	28VDC	0mA	21.5 A	150mA	27.87A	90	5000µF ⁽²⁾
CFB600-24S32	18-36VDC	32VDC	0mA	19 A	150mA	27.84A	91	5000µF ⁽²⁾
CFB600-24S48	18-36VDC	48VDC	0mA	12.5A	200mA	27.47A	91	5000µF ⁽²⁾
CFB600-48S12	36-75VDC	12VDC	0mA	50 A	90mA	13.89A	90	10000µF ⁽²⁾
CFB600-48S24	36-75VDC	24VDC	0mA	25A	100mA	13.59A	92	5000µF ⁽²⁾
CFB700-48S28	36-75VDC	28VDC	0mA	25 A	105mA	16.03A	91	5000µF ⁽²⁾
CFB600-48S32	36-75VDC	32VDC	0mA	19 A	90mA	13.77A	92	5000µF ⁽²⁾
CFB600-48S48	36-75VDC	48VDC	0mA	12.5A	130mA	13.59A	92	5000µF ⁽²⁾

NOTE: 1. Nominal Input Voltage 24, 48 VDC

2. The output terminal of all models required a minimum capacitor 470uF to maintain specified regulation.

SPECIFICATIONS

All Specifications Typical At Nominal Line, Full Load, and 25°C Unless Otherwise Noted

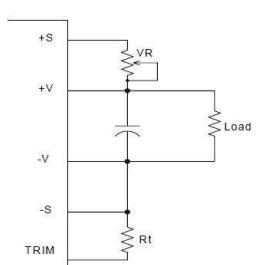
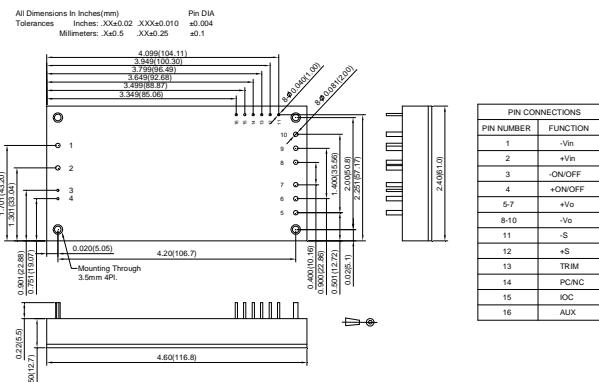
INPUT SPECIFICATIONS:

Input Voltage Range.....	24V	18-36V
	48V	36-75V
Input Surge Voltage (100ms max.)	24V	50Vdc max.
	48V	100Vdc max.
Under voltage lockout	24Vin power up	17V
	24Vin power down	16V
	48Vin power up	35V
	48Vin power down	33V
Input over voltage protection ...	24Vin Turn off	40V, Turn on 38V
	48Vin Turn off	80V, Turn on 77V
Opto isolated Remote ON/OFF (Note6)		
Input Filter		PI Type

OUTPUT SPECIFICATIONS:

Voltage Accuracy	±1.5% max.			
Transient Response:25% Step Load Change	<500us			
External Trim Adj. Range	6	0	-	1
			1	0
				%
Load share Accuracy	±10% at 50% to 100% Full Load			
Auxiliary output voltage/current	10±3Vdc/20mA max.			
Ripple & Noise, 20MHz BW				
12V	60mV RMS max., 120mV pk-pk max.			
24V	100mV RMS max., 240mV pk-pk max.			
28V	100mV RMS max., 280mV pk-pk max.			
32V	120mV RMS max., 320mV pk-pk max.			
48V	200mV RMS max., 480mV pk-pk max			
Temperature Coefficient	±0.03%/°C			
Short Circuit Protection	Continuous			
Line Regulation (Note1)	±0.2% max.			
Load Regulation (Note2)	±0.5% max.			
Over Voltage Protection trip Range,% Vo nom.	115-140%			
Current Limit	110% ~150% Nominal Output			
Start up time	160ms typ.			

CASE FB



The output voltage can be determined by below equations:

$$V_f = \frac{1.24 \times \frac{Rt \times 33}{Rt + 33}}{7.68 + \frac{Rt \times 33}{Rt + 33}}$$

$$Vout = (Vo + VR) \times Vf$$

Unit: KΩ

Vo: Nominal Output Voltage

Rt=6.8KΩ

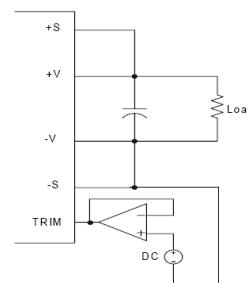
Fig.1 The schematic of output voltage adjusted by using external resistor and/or variable resistor.

GENERAL SPECIFICATIONS:

Efficiency	See Table	
Isolation Voltage	Input/Output	1500VDC min.
	Input/Case	1500VDC min.
	Output/Case	1500VDC min.
Isolation Resistance	10 ⁷ ohm min.	
Isolation Capacitance	4000pF typ.	
Switching Frequency	48S12&48S28&48S32	300KHz typ.
	Others	
Operating Case Temperature	-40°C to 100°C	
Storage Temperature	-55°C to +105°C	
Thermal Shutdown, Case Temp.	110°C typ.	
Humidity	95% RH max. Non condensing	
MTBF	MIL-STD-217F, GB, 25°C, Full Load	450Khrs typ.
Dimensions	4.60x2.40x0.50 inches(116.8x61.0x12.7 mm)	
Case Material	Aluminum Baseplate with Plastic Case	
Weight	220g	

NOTE:

1. Measured from high line to low line.
2. Measured from full load to zero load.
3. Output ripple and noise measured with 10uF tantalum and 1uF ceramic capacitor across output.
4. The output adjustment circuit and trim equations show as figure1 and figure2.
5. An external input capacitor 220uF for all models are recommended to Reduce input ripple voltage.
6. Refer application note Item 5.5.
7. If the remote sense feature is not to be used, the +sense pin should be connected to the +Vout pin and the -sense pin should be connected to the -Vout pin. (refer application note Item 6.9)



Output Voltage = TRIM
Terminal Voltage * Nominal
Output Voltage

Fig.2 The schematic of
output voltage adjusted by
using external DC voltage.